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AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph bridging from page 7, line 23 to page 8, line 11 as

follows.

-- Referring to Figure 3, a set of a cathode unit and an anode unit is illustrated. Each

anode unit of the cathode plate 30 includes an anode conductive layer 32 and a phosphor

layer 33 attached thereon. The anode conductive layer 32 is formed on an anode substrate

31. The cathode plate 30 includes a cathode substrate 41, and each cathode unit includes

a cathode conductive layer 42 formed on the cathode substrate 41 and an electron

emission source layer 43 attached on the cathode conductive layer 42. A mesh 5 is

disposed between the cathode plate 40 and the anode plate 30. The mesh 5 includes a

converging electrode layer 51, an insulation layer 52 and a gate layer 53 stacked together.

The converging electrode layer 51 is facing the anode plate 30, while the gate layer 53 is

facing the cathode plate 40. Each of the gate layer 53 and the converging electrode layer

51 is connected to a specific potential. The mesh 5 includes a plurality of apertures 54

aligned with the corresponding set of anode and cathode units, such that electron emitted

from the electron emission source layer 43 can propagate through the aperture [[51]]54

towards the phosphor layer 33.-

Please replace the paragraph bridging from page 8, line 25 to page 9, line 12 as

follows.

--The field-emission display further comprises a spacing glass plate 34 extending

between the anode plate 10 and the mesh 5. The material for fabricating the spacing glass

plate 34 is preferably the same as that for fabricating the anode substrate 31 and the

cathode substrate 41. The thickness of the spacing glass plate 34 depends on the space

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between the anode plate 10 and the cathode plate 20. In this embodiment, the thickness of the spacing glass plate 34 is about 0.5 mm to about 1.5 mm, for example. A plurality of through holes [[34]]341 are formed extending through the spacing glass plate 34. The through holes [[34]]341 are aligned with the apertures 54 of the mesh 5. As shown in Figure 6, larger through holes [[34]]341 may be formed to cover the range of the apertures 54 of two or more than two sets of anode and cathode units. The spacing glass plate 34 also includes an invalid region 342 along a periphery thereof. Markings 343 can be formed on the invalid region 342 to aid in alignment of the mesh 5 and the anode plate 10. Isolation walls 35 can also be formed between the mesh 5 and the anode substrate 31 by screen printing, such that a specific space can be maintained to the advantage of air conducting channel during package.—

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